

Additional Upstream

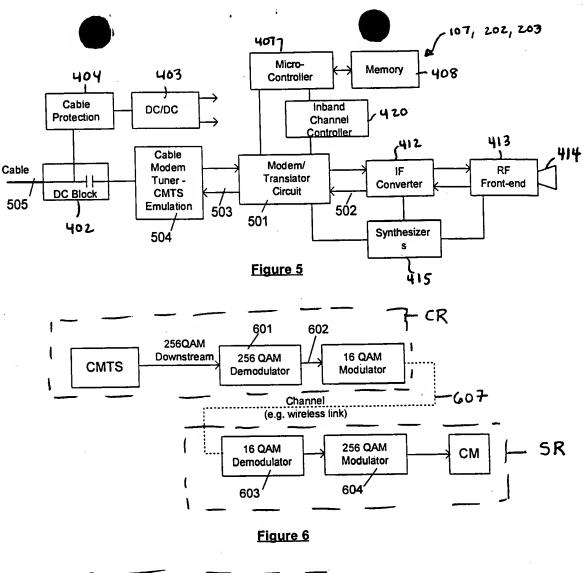
Channel Processor

Figure 4

**Synthesizers** 

417

415



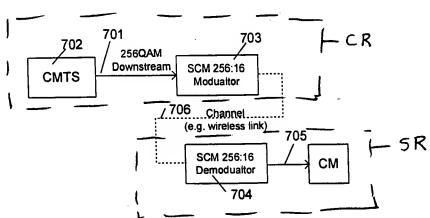


Figure 7

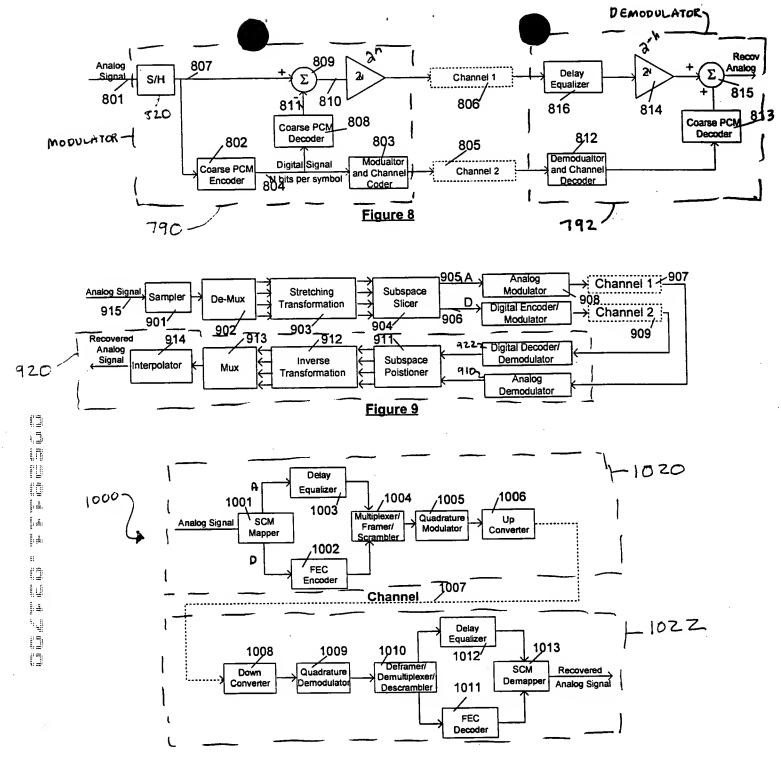


Figure 10

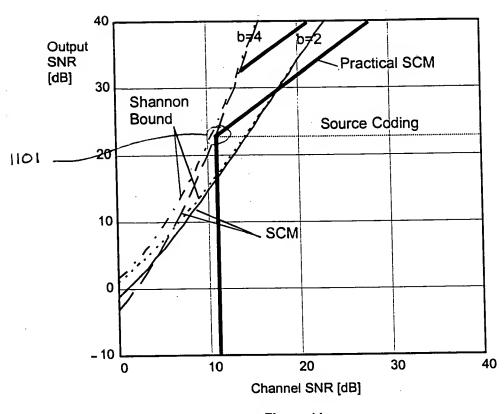


Figure 11

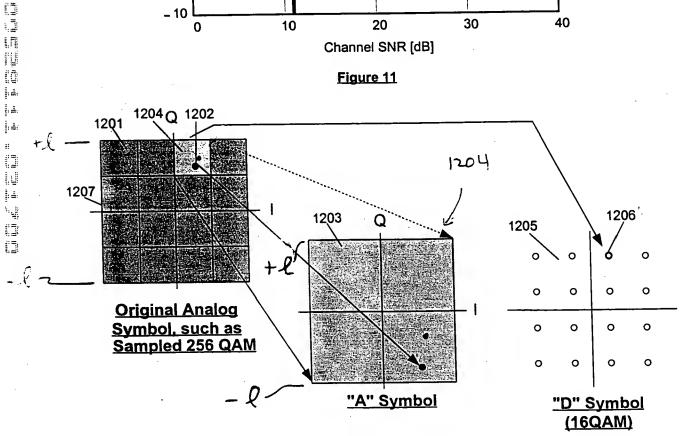
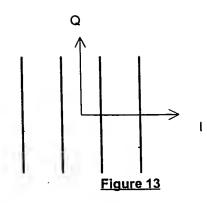


Figure 12



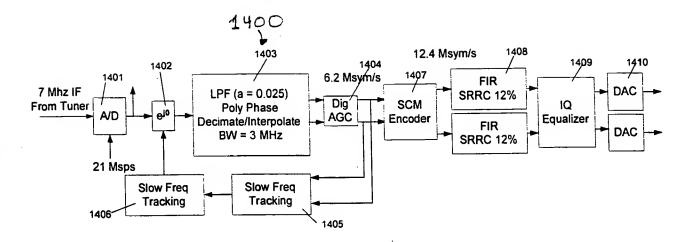


Figure 14

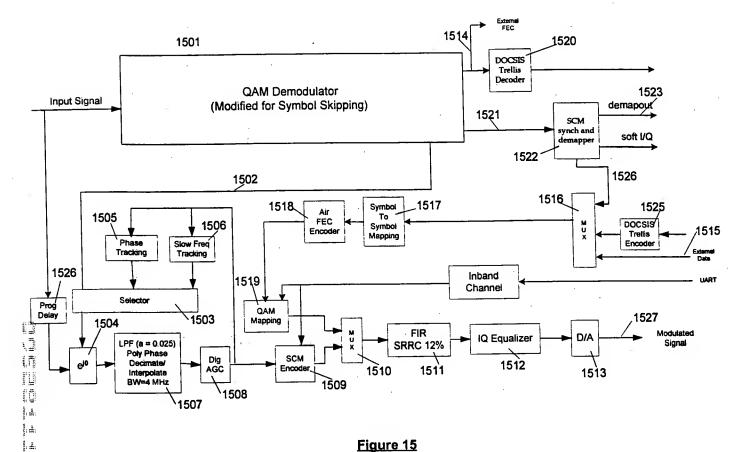


Figure 15

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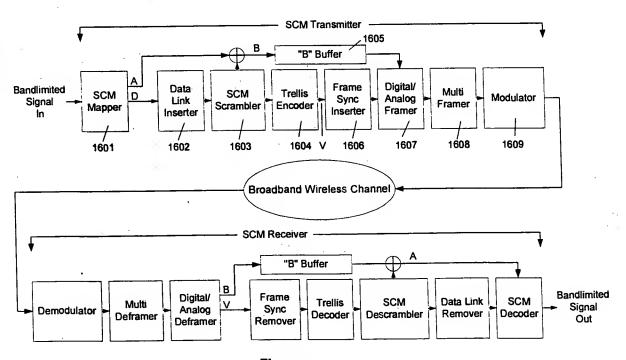


Figure 16

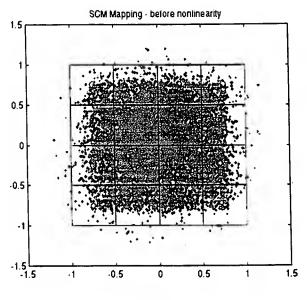


Figure 17

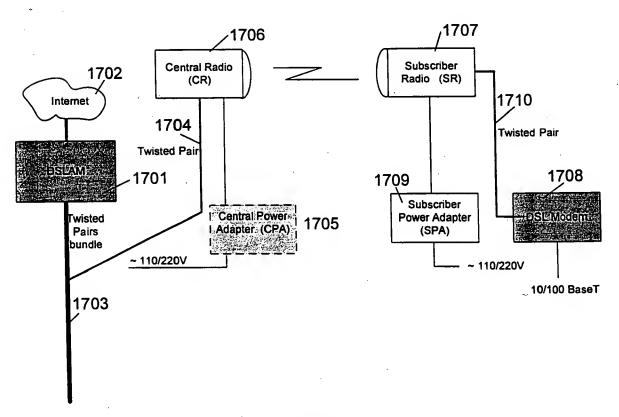


Figure 18